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OSMM&N File No. 214258US2S

Serial No. 09/956,986

In the matter of the Application of: Michiharu MATSUI et al

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE HAVING
ELEMENT ISOLATING REGION OF TRENCH TYPE AND METHOD OF
MANUFACTURING THE SAME

Due Date: 08/09/03

The following has been received in the U.S. Patent Office on the date stamped hereon

- ☒ Dep. Acct. Order Form
- ☒ Letter Submitting Drawing Sheet(s)
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